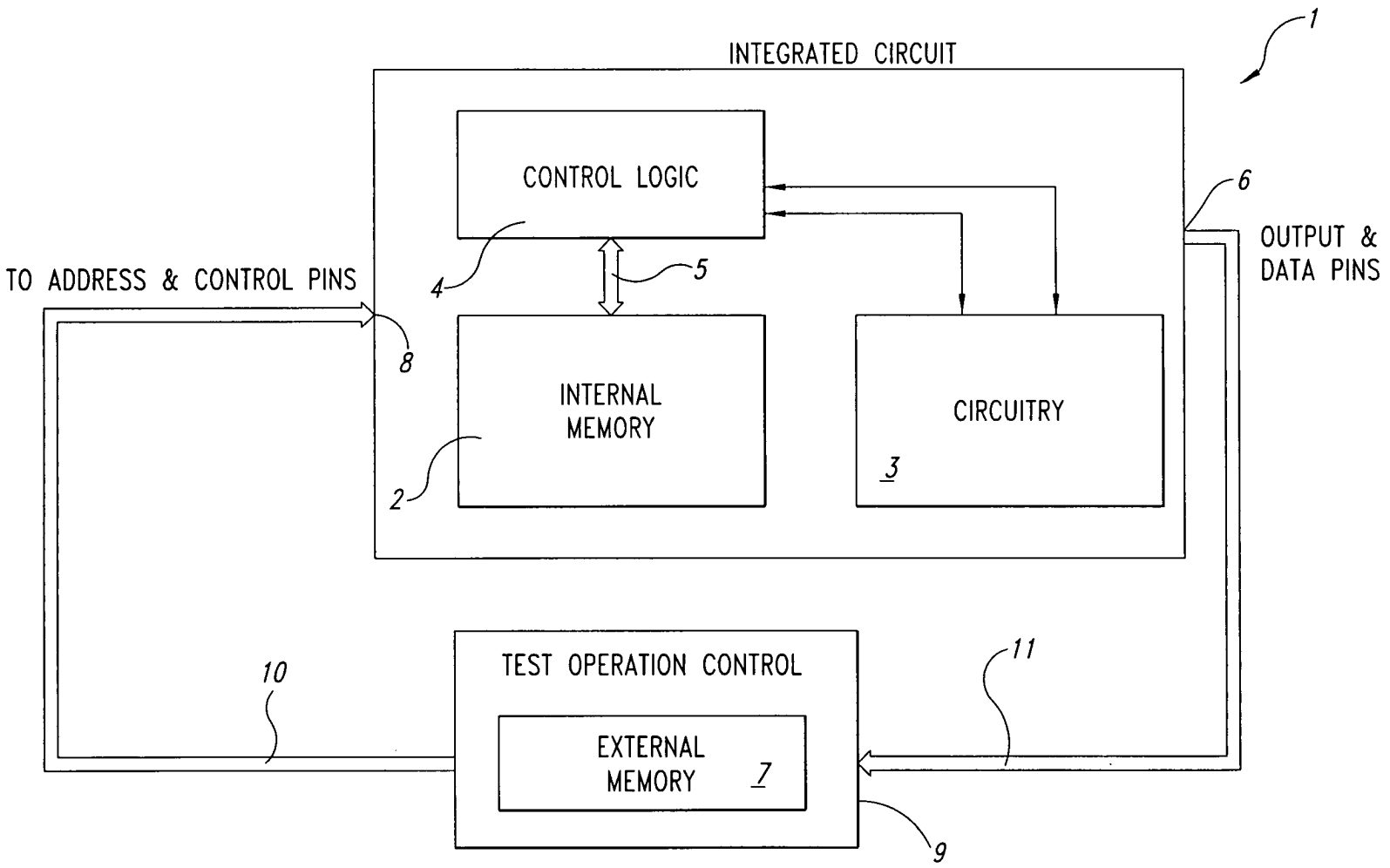
1
INTEGRATED CIRCUIT
CONTROL LOGIC
6
OUTPUT &
DATA PINS
TO ADDRESS & CONTROL PINS
4
5
8
INTERNAL
MEMORY
CIRCUITRY
2
3
10
TEST OPERATION CONTROL
11
EXTERNAL
MEMORY
7
9